

INFORMATION DISCLOSURE CITATION
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APPLICANT(S):

Pavel Horsky et al.

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GROUP ART UNIT:

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>Lu</i>	A.	4,292,625	09-29-1981	Schoeff			
	B.	2003/0016086	01-23-2003	Yang			
	C.	6,507,304	01-14-2003	Lorenz			
	D.	6,037,888	03-14-2000	Naim			
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	F.	5,841,384	11-24-1998	McKay			
	G.	6,329,940	12-11-2001	Dedic			
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<i>Lu</i>	J.	5,844,511	12-01-1998	Izumikawa			

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							YES	NO
<i>L</i>	K.	EP 0 359 315	03-21-1990	EP				
<i>L</i>	L.	EP 0 397 240	11-14-1990	EP				
	M.							
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>L</i>	R.	Chin et al., "A 10-b 125 MHz CMOS Digital-to-Analog Converter DAC with Threshold Voltage Compensated Current Sources," IEEE Journal Solid State Circuits, pp. 1374-1380, November 11, 1994.
<i>L</i>	S.	Sumanen et al., "A 10-bit High-Speed Low-Power CMOS D/A Converter in 0.2 mm," Electronics, Circuits and Systems, 1998 IEEE International Conference on Lisboa, Portugal, pp. 15-18, September 7, 1998.
<i>L</i>	T.	Communication Pursuant to Article 96(2) EPC, pp. 1-6 (October 13, 2005).
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EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.